

CPLD

is module connect with ET-BASIC I/O V1.0 for study CPLD (Complex Programmable Logic Device) Chip. It's new technology to design Digital Circuit easily and quickly in one IC. CPLD which is designed internal circuit with basic logic can write and delete program frequently. ET-CPLD TRAINING MODULE XC95108 uses CPLD from XILINX No.XC95108 size 84 PIN PLCC TYPE with 2400 gate.

● **XC95108 108 Macrocells 2400 Usable Gates**

SPECIFICATIONS CPLD XC95108 FROM XILINX

- 7.5 ns PIN TO PIN LOGIC DELAYS ON ALL PINS
- 108 MACROCELLS WITH 2400 USABLE GATES
- 5V IN-SYSTEM PROGRAMMABLE (ISP)
- ENDURANCE OF 10000 PROGRAM /ERASE CYCLES
- HIGH-DRIVE 24 mA outputs
- 3.3 V or 5 V I/O CAPABILITY
- 84 PIN PLCC TYPE

ET-CPLD TRAINING MODULE (P-ET-A-00133)

XC95108
108 Macrocells
2400 Usable Gates



Program development ET-CPLD TRAINING MODULE with CPLD, users can write program with SCHEMATIC System or HDC Language of XILINX Foundation Series XILINX WEBPACK Program. Write and design program on Computer PC, then download data through PRINTER PORT with JTAG Wire Connector into CPLD XC95108 directly.

● **XC953636 Macrocells 800 Usable Gates**

SPECIFICATION XC9536 (FEATURES)

- 5 ns PIN TO PIN LOGIC DELAY ON ALL PINS
- FCNT TO 100MHZ, 36 MACROCELLES WITH 800 USABLE GATES
- IN - SYSTEM PROGRAMMABLE 10,000 PROGRAM / ERASE CYCLES
- HIGH - DRIVE 24mA OUTPUT
- UP TO 34 USER I/O PIN
- 3.3V OR 5V I/O CAPABILITY
- SUPPLY VOLTAGE 3.3V
- SUPPORT PARALLEL PROGRAMMING OF MORE THAN ONE XC9500 CONCURRENTLY

ET-CPLD V2 (P-ET-A-00145)

XC9536
36 Macrocells
800 Usable Gates

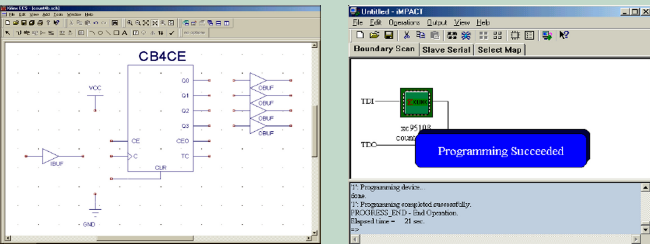


- ET-CPLD V2.0 BOARD IS DESIGNED TO DEVELOP PROGRAM EASILY, BE ABLE TO DOWNLOAD INTO IC CPLD FROM COMPUTER PC WITH IN CIRCUIT DOWNLOAD
- CPLD NO.XC9536XL-10 FROM XILINX WITH 800 GATES 44 PIN PLCC TYPE

- BE ABLE TO CONNECT ET-CPLD V2.0 BOARD WITH ET-BASIC I/O V1, BE ABLE TO EXPERIMENT FOLLOW AS THE BOOK NAMED "LEARN AND UNDERSTAND XC95108" FROM ETT BUT USERS MAY BE CHANGE SOME METHOD TO EXPERIMENT

- 7805 (5V) AND LD1086 (3.3V)REGULATOR POWER SUPPLY ON BOARD
- POWER SUPPLY 7-12VDC
- PCB SIZE 6.2 x 8.1 CM.
- ET-CPLD V2.0 INCLUDES;

- ET-CPLD V2 BOARD
- USER AMNUAL
- CD-ROM
- CABLE DOWNLOAD ET-CAB10P CABLE

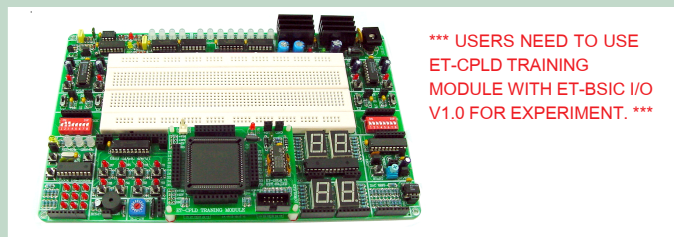


- Writing program with VHDL Language
- In-System Programming

ETT's CPLD includes; ET-CPLD TRAINING BOARD, Manual named "Learn and Understand CPLD XC95108"

- INTRODUCTION OF CPLD
- PROGRAM INSTALL
- USING XILINX
- USING TRAINING BOARD
- 25 WORK SHEETS OF DIGITAL CPLD such as

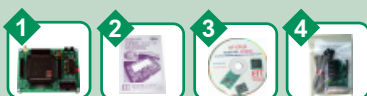
AND Gage Circuit, OR Gage Circuit, NOT Gage Circuit, NAND Gage Circuit, NOR Gage Circuit, Exclusive-OR Gage Circuit, Exclusive NOR Gage Circuit, RS FLIP-FLOP, D FLIP-FLOP, JK FLIP-FLOP, HALF ADDER, FULL SUBTRACTOR, Multiplexer Circuit, D- Multiplexer Circuit, Decoder Circuit, Comparator Circuit, Shift Register Circuit, Counter Circuit, Translator BCD as 7-SEGMENT, Clock



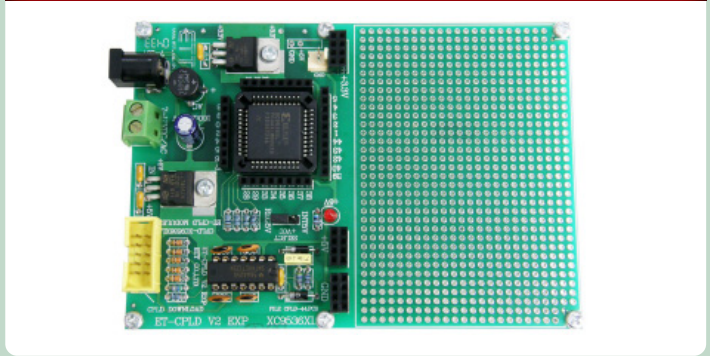
*** USERS NEED TO USE ET-CPLD TRAINING MODULE WITH ET-BASIC I/O V1.0 FOR EXPERIMENT. ***

ET-CPLD TRAINING MODULE XC95108 includes;

- CPLD TRAINING MODULE XC95108 BOARD
- USER MANUAL OF CPLD
- CD-ROM ET CPLD PROGRAM
- DOWNLOAD CABLE OF JTAG PROGRAM



ET-CPLD V2.0 EXP (P-ET-A-00146)



- SPECIFICATIONS ET-CPLD V2.0 EXP IS AS SAME AS ET-CPLD V2.0. THERE'S EXPANSION PCB AS PROTOTYPE WORKING AREA OR AD-102 PROJECT BOARD

- PCB SIZE 13 x 9 CM. AND PROTOTYPE WORKING AREA SIZE 6.2 x 9 CM.
- POWER SUPPLY 7-12VDC
- ET-CPLD V2.0 EXP INCLUDES;

- ET-CPLD V2.0 EXP BOARD
- USER MANUAL
- CD-ROM,
- CABLE DOWNLOAD ET-CAB10P V2

